



Agenda

Workshop Introduction by Dr. Mark Gardner	08:30 – 08:45
Applications & Algorithms I	08:45 – 10:00
<ul style="list-style-type: none">• <i>Multilevel Parallelism for Computational Fluid Dynamics Codes on Heterogeneous Platforms</i> Prof. Chris Roy	[08:45-09:10]
<ul style="list-style-type: none">• <i>cuART: Fine-Grained Algebraic Reconstruction Technique for Computed Tomography Images on GPUs</i> Xiaodong Yu	[09:10-09:35]
<ul style="list-style-type: none">• <i>Transforming Irregular Applications for Parallel Computing Architectures</i> Jing Zhang	[09:35-10:00]
Break and Posters	10:00 – 10:20
Models of Communication and Computation	10:20 – 11:10
<ul style="list-style-type: none">• <i>Models and Techniques for Power-Aware High-Performance Computing</i> Vignesh Adhinarayanan	[10:20-10:45]
<ul style="list-style-type: none">• <i>Characterizing and Tuning Disk I/O for HPC Systems</i> Sarunya (“Kwang”) Pumma	[10:45-11:10]
Applications & Algorithms II	11:10 – 11:35
<ul style="list-style-type: none">• <i>Identification of novel miRNA-mediated regulation in oncogenic pathways</i> Sharmi Banerjee (Remote)	[11:10-11:35]
Lunch [Not Provided]	11:35 – 13:05
Libraries and Frameworks	13:05 – 14:45
<ul style="list-style-type: none">• <i>GLAF: A Framework for Enhancing Performance, Programmability and Portability of Heterogeneous Platforms</i> Konstantinos Krommydas	[13:05-13:30]
<ul style="list-style-type: none">• <i>MetaMorph: A Library Framework for Interoperable Kernels on Multi- and Many-core Clusters</i> Ahmed Helal	[13:30-13:55]
<ul style="list-style-type: none">• <i>Portability Problems: Platforms, Performance, and Programming Paradigms</i> Paul Sathre (Remote)	[13:55-14:20]
<ul style="list-style-type: none">• <i>Cognizant Networks: A Model for Session-based Communications and Adaptive Networking</i> Umar Kalim	[14:20-14:45]
Break and Posters	14:45 – 15:05
Accelerator-based Computing	15:05 – 16:30
<ul style="list-style-type: none">• <i>Introduction to Automata Processing (“Think Processor in Memory”)</i> Xiaodong Yu	[15:05-15:30]
<ul style="list-style-type: none">• <i>Directive-Based Pipeline Extensions for Accelerators in High Performance Computing</i> Xuewen (“Harry”) Cui	[15:30-15:55]
<ul style="list-style-type: none">• <i>Automatic Vectorization of Parallel Algorithms on Modern Multi/Many-core Processors</i> Kaixi Hou (Remote)	[15:55-16:20]
Sit-Down Q&A Discussion and Wrap-Up: Dr. Hao Wang	16:20 – 17:00